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	oll No rinted Pa	ages: 2		8510		
			/D-13			
	MIC	CROPROCESSOR				
		Paper-E0	CE-311-E			
Ti	me allov	wed: 3 hours]	[M	aximum marks : 100		
Ne		empt any five question m each unit.	s by selecti	ng at least one question		
		Un	it-I			
1.	(a)	· ·				
		the functions of BIU	J and EU.	15		
	(b)	Discuss the role of v	vait state ir	the operation of 8086		
		microprocessor. Ho	w it is gene	erated? 5		
2. (a) What is the di			ence betw	een CISC and RISC		
		instructions?		5		
	(b)	What are the function	ns of IC 828	84 in the 8086 systems?		
	(c)	Explain the function	of aucue			
	(d)	-	-	guration of Intel's 8086		
	()	PSW format.		5		
Unit-II						
3.	(a)	State and explain th	e different	instruction formats of		
		8086.		10		
	(b)	Explain the following	4.5			
		OFFSET, ASSUMI	E, PROC,	PUBLIC, EXTRN.		
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(2)

		(4)	
4.	(a) (b)	 What is the difference between the instruction (i) MOV AX, TABLE-ADDR and LE TABLE-ADDR (ii) NEG BYTE PTR [BX] and NOT BYTE [BX]. Write a program that reverses an array of thirty integers. The array should be reversed in place, copying to another array. 	A AX. TE PTR 10 y 16-bit
		Unit-III	10
5.	(a)	What do you understand by the term I controller? Discuss the working of TMS 4500 coin brief.	
	(b)	Explain 8086 interfacing to memory.	8
6.	opera inter 8086 the d done	t do you understand by minimum and maximum attion of 8086? Draw the circuit diagram show facing of 8086 with 2 × 512 K of memory. Assu is to be used in minimum mode. Explain clear emultiplexing of address, data and control bus he. Detail the sequence of events that takes plaing an instruction code from the memory.	ing the me that ly how as been
7	(-)	Unit-IV	
7.	(a) (b)	Explain scrial I/O port design. What is the difference between memory mappand isolated I/O? Give advantages and disadva of each.	
	(c)	Explain interfacing of 8086 with any high device.	power 6
8.	(a)	Explain the interrupts and interrupt response of	f 8086. 10
_	(b) 8510	Explain applications of hardware interrupt.	10
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