Roll No. ....

Printed Pages : 2

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BT–4 / M–17

COMPUTER ARCHITECTURE &
ORGANIZATION

Paper–CSE-202 E

Time allowed : 3 hours]   *[Maximum marks : 100*

Note : *Attempt only five questions. All questions carry equal marks.*

1. Describe Flynn's classification of computers in detail with appropriate examples.   20

2. (a) Explain Instruction Set Architecture in detail.   10

   (b) Explain different types of instructions applied on instruction set with examples.   10

3. Define addressing modes. Explain different types of it with examples.   20

4. (a) Design the micro instruction sequence counter.   10

   (b) Explain page and file organization with suitable example.   10

5. Explain accumulated based CPU architecture types and solve the equation $X = (A + B) * (C + D)$ using above said architecture.   20

6. How many 128×8 RAM chips needed to provide memory capacity of 2048 Bytes ? How many lines of address must be

8401   P.T.O.

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used to access 2048 bytes of memory? How many of these lines will be common to all chips ? How many lines must be decoded for line select ?   20

7. (a) Explain direct mapped cache organization.   10

   (b) Explain processor level parallelism with example.   10

8. Write short notes on following :

   (a) DMA and Interrupts   7

   (b) Basics of Logic Design   7

   (c) Stack Organization   6

8401