Roll No.

Total Pages: 02

BT-5/D-18.

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P.T.O.

COMPUTER ORGANIZATION AND ARCHITECTURE CSE-307N

Time: Three Hours

(3-103/3) L-35116

[Maximum Marks: 75

Note: Attempt Five questions in all, selecting at least one question from each Unit. All questions carry equal marks.

Unit I

- Explain the Digital arithmetic algorithm for addition (a) with example.
 - Discuss division using non-restoring algorithm with (b) 7 help of flowchart.
- Explain the organization and architecture of a (a) computer system.
 - Describe Flynn's Classification of computer (b) architecture.

3.	Discuss		the	following
	(a)	М	icro	program

Micro programmed control.

Register reference instructions.

What is the difference between an immediate, a direct and an indirect address instructions? How many references are needed for each type of instructions to bring an operand into a processor register? 15

Unit III

What do you mean by speed up factor in pipelining? 5. (a) State Amdahl's Law.

What are the characteristics of RISC architecture? How it is different from CISC architecture?

What are the different addressing modes? Explain with examples. Also list their advantages and disadvantages.

Unit IV

Write short note on DMA controller and transfer. 7. (a)

(b) What do you mean by input out processor? Explain.

Write short notes on the following:

Asynchronous Serial Transfer (a)

(b) Daising Chaining.

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