Roll No.

Total Pages: 03

37015 BT-7/D-19

ADVANCED MICROPROCESSORS **ECE-423E**

[Maximum Marks: 100 Time: Three Hours]

Note: Attempt Five questions in all, selecting at least one question from each Unit.

Unit I

- Explain the concept of pipelining for X86 (a) microprocessors. Support your answer with a suitable diagram and explain the different stages of pipelining. 10
 - Define data format and give all the data formats supported by Pentium Processor. 10
- With the help of a suitable diagram explain in 2. (a) detail the functioning of all the registers present in X86 families of processors. 5
 - Draw the Instruction format of Pentium processor and explain the function of each bit.

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Explain in detail the concept of memory (c) segmentation in Pentium Processor ? Explain in detail the segmentation scheme used in protected mode for X86 microprocessor processors.

Unit II

- 3. internal architecture of (a) the Draw 80386 microprocessor and explain its functioning in detail. 15
 - (b) A page linear address for IA-32 processor is 23415623H. Formulate a physical address out of it and represent the conversion with the help of a diagram.

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- Explain the functioning of different pins of 80286 4. (a) Processor. http://www.kuonline.in 15
 - Draw and explain the system segment descriptors used in 80286 processor.

Unit III

- Draw the internal architecture of the mathematical (a) co-processor for 80286 and also explain the register set of the co-processor. 10
 - Interface 80286 with its numeric co-processor and show the necessary block diagram for the same. 10

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(3-35/6) L-37015

6.	(a)	Explain the different instructions supporting	
		transcendental operations. Also explain the	
		functioning of different control instructions for 80286	
		microprocessor. 10	
	(b)	Explain the functioning of different pins of 80286	
		microprocessor. 10	
		-	
Unit IV			
7.	(a)	Define interrupts and explain the interrupt structure	
		for X87 family of processors.	
	(b)	Draw the internal architecture of 80487	
	` '	microprocessor and explain its functioning in detail.	
		10	
8.	Expl	Explain the following:	
	(a)	Protection Mechanism 5	
	(b)	Assembler Directives 5	
	(c)	Branch Prediction 5	
	(d)	80387 Microprocessor. 5	
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